

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	58	@ad<"20050606" and (simulat\$3 with analog) and (simula\$3 with RF)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/06/04 16:45
S3	310	@ad<"20050606" and (simulat\$3 with mixed with signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/05/31 15:45
S5	310	@ad<"20050606" and (simulat\$3 with mixed with signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/06/01 08:12
S6	4	S5 and (simulat\$3 with analog) and (simula\$3 with RF)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/06/01 10:40
S7	2	"20030046045" "20030009732"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/06/01 17:02
S8	1	10/551571	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/06/01 10:50
S9	1	"6181754".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/06/04 08:38
S11	1	"4791593".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/06/04 16:10
S12	1	"4791593".pn. and (relax\$3 or relaxation)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/06/04 16:10

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S13	236	@ad<"20050606" and (simulat\$3 with circuit) and ( distributed with simulation) and network	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/06/04 16:50
S14	82	@ad<"20050606" and (simulat\$3 with circuit) and ( distributed with simulation with network) and network	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/06/04 16:55
S15	82	@ad<"20050606" and (simulat\$3 with circuit) and ( distributed with simulation with network)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/06/04 16:55

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AV Oppenheim, RW Schafer - 1989 - Prentice-Hall, Inc. Upper Saddle River, NJ, USA  
 ... 27th conference on Winter **simulation**, p.865 ... Cota , Marcelo Lubaszewski, Improving  
**mixed-signal SOC** ... Sampled-Data Signal Processing, **Analog** Integrated Circuits ...  
 Cited by 3887 - [Related Articles](#) - [Web Search](#) - [Library Search](#)

[book] [Steady-state Methods for Simulating \*\*Analog\*\* and Microwave Circuits - all 2 versions »](#)

KS Kundert, JK White - 1990 - books.google.com  
 ... **circuit** designers had in **simulation** techniques that could ... steady-state solutions  
 of **analog** and microwave ... to harmonic balance and **mixed frequency-time** methods. ...  
 Cited by 212 - [Related Articles](#) - [Web Search](#) - [Library Search](#)

[Efficient computation of quasi-periodic \*\*circuit\*\* operating conditions via a \*\*mixed frequency/time\*\* ... - all 16 versions »](#)

D Feng, J Phillips, K Nabors, K Kundert, J White - Proc. of 36 IEEE Design Automation Conference,  
 1999 - doi.ieeecomputersociety.org  
 ... and circuits used in **RF** communications systems ... methods[4], thus avoiding long transient  
**simulation times**. ... To circumvent these difficulties, **mixed frequency-time** ...  
 Cited by 16 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Fast \*\*simulation\*\* algorithms for \*\*RF\*\* circuits - all 3 versions »](#)

R Telichevesky, K Kundert, I Elfadel, J White, CDS ... - Custom Integrated Circuits Conference, 1996.,  
 Proceedings of ..., 1996 - ieeexplore.ieee.org  
 ... greatly expanded the need for **simulation** algorithms that ... distortion, and noise of  
**RF** communica- tion ... linear **time-varying**, and **mixed frequency-time** techniques [6 ...  
 Cited by 48 - [Related Articles](#) - [Web Search](#)

[A \*\*mixed frequency-time\*\* approach for distortion analysis of switching filter circuits - all 3 versions »](#)

KS Kundert, J White, A Sangiovanni-Vincentelli - Solid-State Circuits, IEEE Journal of, 1989 -  
 ieeexplore.ieee.org  
 ... paper we present a new **mixed frequency-time** approach for ... N GENERAL, **analog circuit**  
 designers rely heavily I on **circuit simulation** programs like SPICE [I] or ...  
 Cited by 20 - [Related Articles](#) - [Web Search](#) - [Library Search](#)

[Time-domain non-Monte Carlo noise \*\*simulation\*\* for nonlinear dynamic circuits with arbitrary ... - all 9 versions »](#)

A Demir, EWY Liu, AL Sangiovanni-Vincentelli - Computer-Aided Design of Integrated Circuits and  
 Systems, ..., 1996 - ieeexplore.ieee.org  
 ... of especially **analog** subblocks in a **mixed-signal** ... **simulator** and widely used by **analog**  
**circuit** designers ... of the **circuit** before the transient **simulation** is started ...  
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[Trends in current \*\*analog\*\* design-a panel debate - all 2 versions »](#)

O Aaserud, IR Nielsen - **Analog** Integrated Circuits and Signal Processing, 1995 - Springer  
 ... Growing requirements for single chip **mixed VLSI** designs ... for more **analog** system level  
**simulation**, has generally ... classical solution with the **analog RF-IF** mixer ...  
 Cited by 35 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

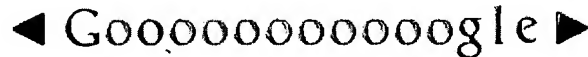
[A methodology for efficient high-level dataflow \*\*simulation\*\* of \*\*mixed-signal\*\* front-ends of digital ... - all 13 versions »](#)

G Vandersteen, P Wambacq, Y Rolain, P Dobrovolný, ... - Proc. DAC, 2000 -  
 doi.ieeecomputersociety.org

... **simulation**, at the architectural level, of **mixed**-signal front ... This yields very long **simulation** results. ... used for system-level simulations of **analog** front-ends [2 ...  
[Cited by 20](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[An oversampling delta-sigma frequency discriminator - all 2 versions »](#)  
RD Beards, MA Copeland - Circuits and Systems II: **Analog** and Digital Signal ..., 1994 -  
ieeexplore.ieee.org  
... TRm ACTIONS ON CIRCUITS AND SYSTEMS-11: **ANALOG** AND DIGITAL ... The **simulation** and  
the  
Fourier analysis on the ... were performed using the Saber **mixed**-mode **simulator** ...  
[Cited by 27](#) - [Related Articles](#) - [Web Search](#)

[CAD solutions and outstanding challenges for mixed-signal and RF ICdesign - all 8 versions](#)  
»  
D Leenaerts, G Gielen, RA Rutenbar - Computer Aided Design, 2001. ICCAD 2001. IEEE/ACM ...,  
2001 - ieeexplore.ieee.org  
... Synthesis flow for **Analog & Mixed**-Signal 272 ... the merit of some evolving **analog circuit**  
or layout ... numerical optimization with full SPICE **simulation**, making it ...  
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 JL Pino, K Kalbasi, EE Div - Signals, Systems & Computers, 1998. Conference Record of the ..., 1998 - [ieeexplore.ieee.org](#)

 ... synchronous dataflow enables efficient **cosimulation** between the ... the codesign of the various **analog RF** and DSP ... JEURSim [2] Co-Design/Co-Simulation Lab, Hewlett ...

[Cited by 15](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)
[A frequency relaxation approach for analog/RF system-level simulation - all 6 versions »](#)

 X Li, Y Xu, P Li, P Gopalakrishnan, LT Pileggi - Proceedings of the 41st annual conference on Design ..., 2004 - [portal.acm.org](#)

 ... the traditional algorithms for **circuit-level** simulation ... SPW and COSSAP is effective for **analog/digital co-simulation**. However, for an **analog/RF** system, the wide ...

[Cited by 3](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)
[Analysis and compact behavioral modeling of nonlinear distortion in analog communication circuits](#)

 P Dobrovolny, G Vandersteen, P Wambacq, S Donnay, ... - Computer-Aided Design of Integrated Circuits and Systems, ..., 2003 - [ieeexplore.ieee.org](#)

 ... bottom-up models of nonlinear **analog** continuous-time ... to enable their efficient **cosimulation** with the ... gain in simulation efficiency of **RF** integrated circuits at ...

[Cited by 6](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)
[CAD for RF circuits - all 13 versions »](#)

 P Wambacq, G Vandersteen, J Phillips, J ... - Design, Automation and Test in Europe, 2001 - [doi.ieeecomputersociety.org](#)

 ... Efficient **co-simulation** of **RF** blocks with **analog** baseband blocks is obtained in FAST by using a multicarrier signal representation, which is an ...

[Cited by 4](#) - [Related Articles](#) - [Web Search](#)
[High-level simulation and modeling tools for mixed-signal front-ends of wireless systems - all 2 versions »](#)

 P Wambacq, G Vandersteen, P Dobrovolný, M ... - Proc. Advances in Analog Circuit Design, 2002 - [electfp.vub.ac.be](#)

 ... To this purpose the **analog** blocks are translated before ... Efficient **co-simulation** of **RF** blocks with low-frequency blocks ... COSSAP [11] to co-simulate RF blocks and ...

[Cited by 5](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#)
[Computer-aided design of analog and mixed-signal integrated circuits - all 2 versions »](#)

 GGE Gielen, RA Rutenbar - Proceedings of the IEEE, 2000 - [ieeexplore.ieee.org](#)

 ... Even fields like **RF**, where traditionally GaAs and bipolar ... are then verified using detailed **cosimulation** techniques. ... The most widely used **analog** CAD tool today ...

[Cited by 141](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)
[Adaptive baseband/RF predistorter for power amplifiers through instantaneous AM-AM and AM-PM ... - all 2 versions »](#)

 EG Jeckeln, F Beaugard, MA Sawan, FM Ghannouchi - Microwave Symposium Digest., 2000 IEEE MTT-S International, 2000 - [ieeexplore.ieee.org](#)

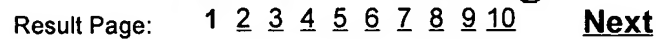
 ... The disturbing effect of gain and phase imbalances of a **RF analogue** quadrature demodulator are completely avoided by ... **RF/DSP co-simulation** and experimental ...

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[Behavioral modeling and simulation of a mixed analog/digital automatic gain control loop in a 5 GHz ... - all 9 versions »](#)

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